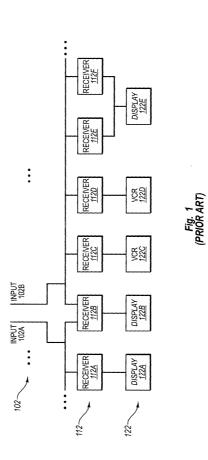
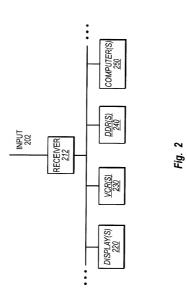
1/14



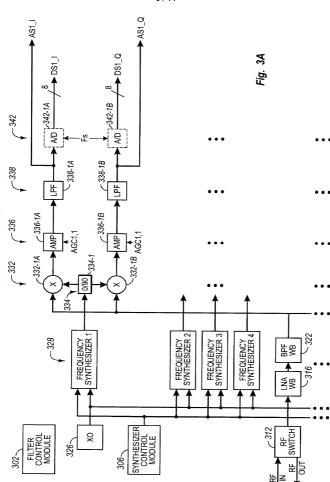
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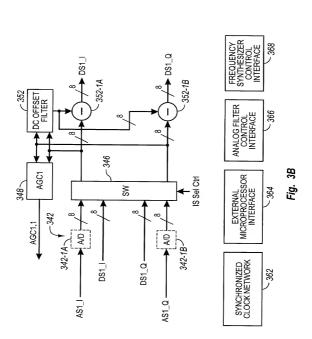
3/14



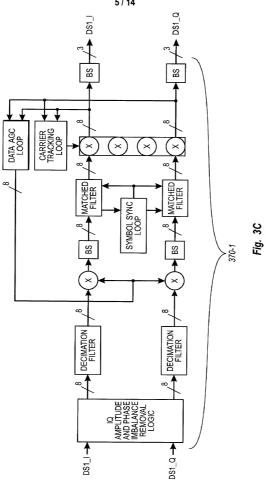
Inventor: Dan Tu, et al. Docket No.: 14531.109

. . . .

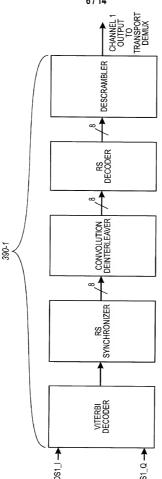
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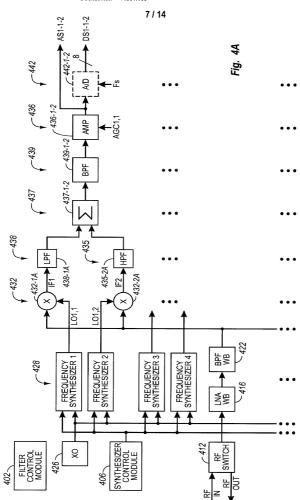




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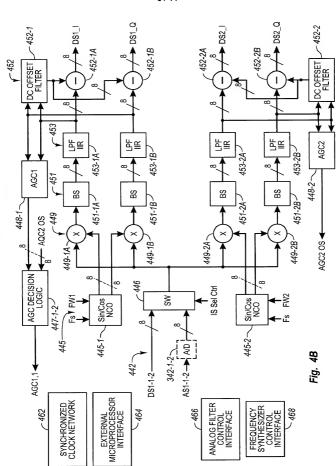


-ia. 3D



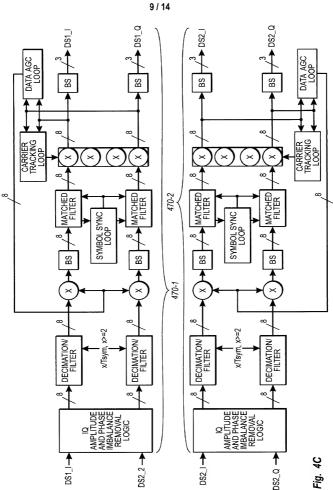
Inventor: Dan Tu, et al. Docket No.: 14531.109

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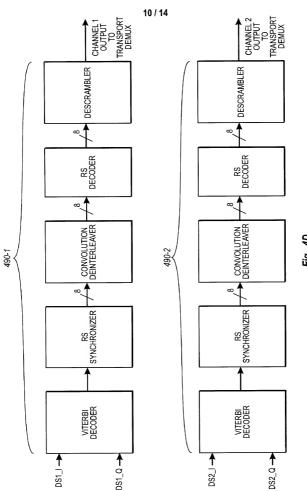


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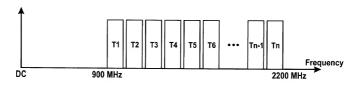


FIG. 5A

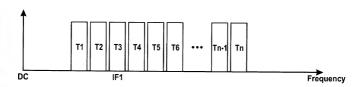


FIG. 5B

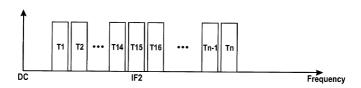


FIG. 5C

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FIG. 5D

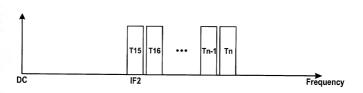


FIG. 5E

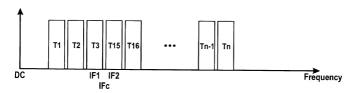


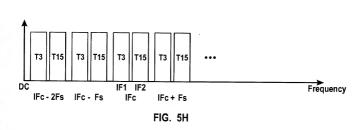
FIG. 5F

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FIG. 5G



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d for	Unit Required fo	Unit Required for	Analog Component/Function
version	Direct Down Conve	IF Sub-sampling	(8 Independent and
3B)	(Figs 3A & 3B)	(Figs 4A & 4B)	Simultaneous Tuners)
	1 (312)	1 (412)	RF Switch
	1 (316)	1 (416)	Low Noise Amp (WB)
	1 (326)	1 (426)	XO
	8 (328)	8 (428)	Frequency Synthesizer
	1 (322)	1 (422)	BPF (WB)
	16 (332)	8 (432)	Mixer
	0	4 (438)	Variable (3db Cutoff Freq) LPF
-	0	4 (435)	Variable (3db Cutoff Freq) HPF
	0	4 (439)	Variable (3db BW) BPF
	0	4 (437)	Summer
	16 (336)	4 (436)	Control Amplifier
	8 (344)	0	90 Degree Phase Splitter
	16 (342)	4 (442)	Analog to Digital Converter
			Additional Digital Component/Function
	N/A	16 (449)	Multiplier
	N/A	8 (445)	NCO (Sin/Cos)
_	NA	0 (443)	